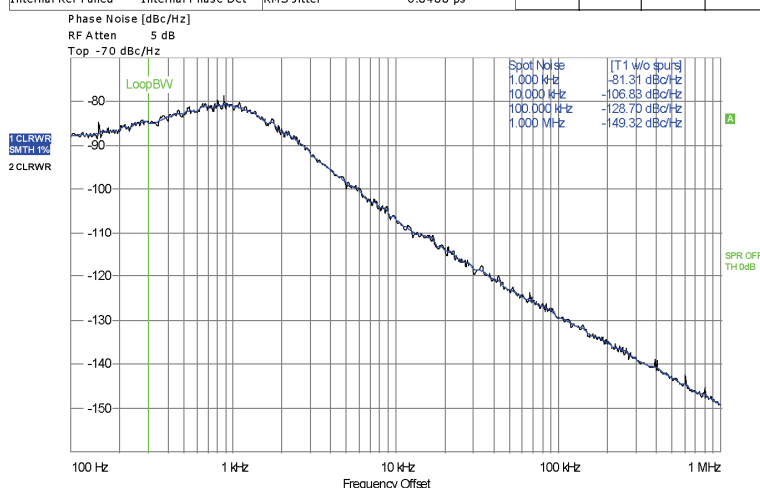


R&S FSUP Signal Source Analyzer				LOCKED
Settings		Residual Noise [T1 w/o spurs]		Phase Detector +30 dB
Signal Frequency:	915.000006 MHz	Int PHN (100.0 .. 1.0 M)	-49.2 dBc	
Signal Level:	2.11 dBm	Residual PM	0.280 °	
Cross Corr. Mode	Harmonic 1	Residual FM	49.796 Hz	
Internal Ref Tuned	Internal Phase Det	RMS Jitter	0.8488 ps	



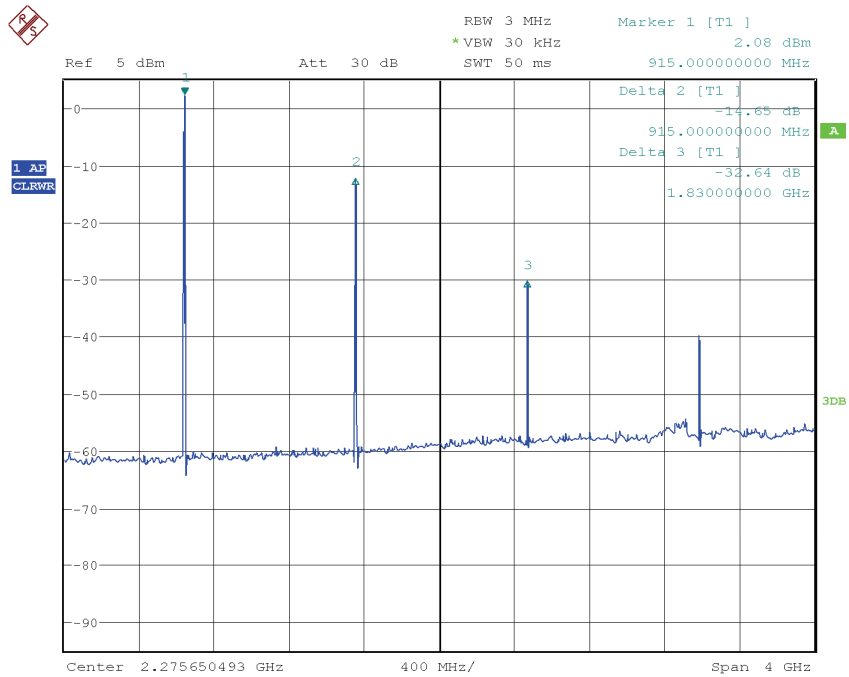
FEATURES

- ◆ Low Phase Noise
- ◆ Fast Settling Time
- ◆ Small Size, Surface Mount
- ◆ Integrated VCO, PLL IC, Loop Filter

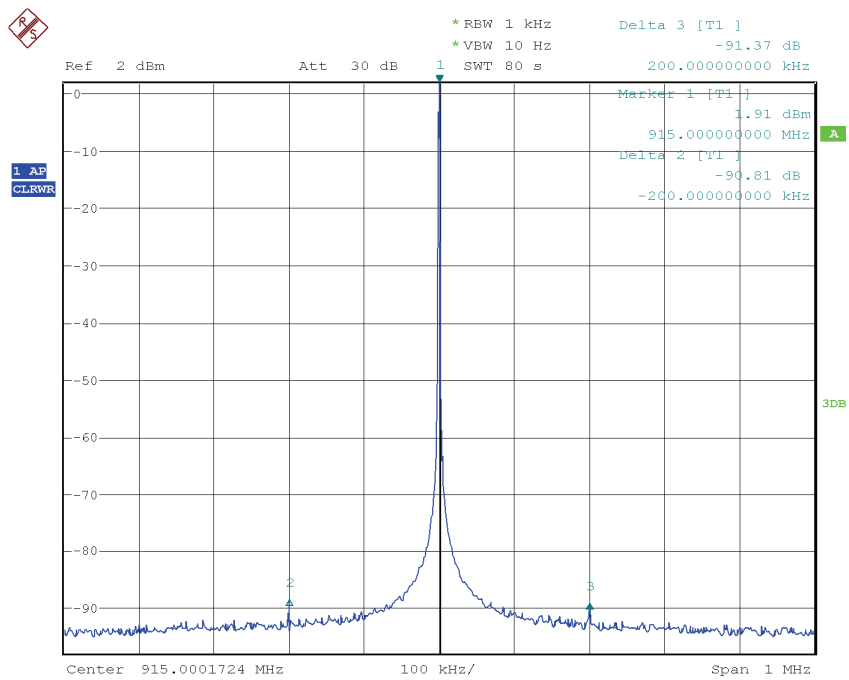
APPLICATIONS

- ◆ Commercial Wireless Applications

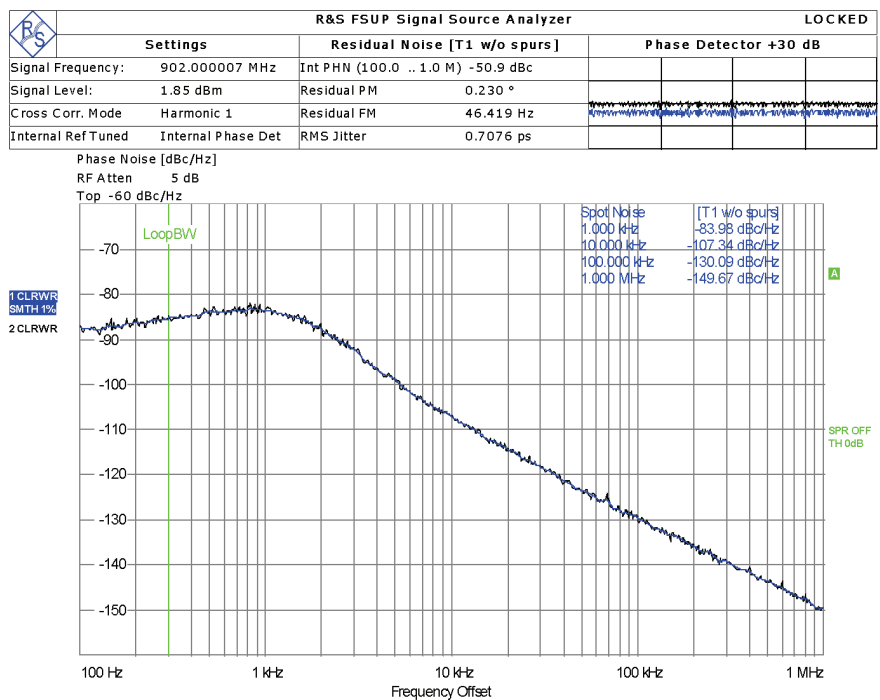
Parameter	Min	Typ	Max	Units
Frequency Range -	902		928	MHz
Step Size		200		KHz
Startup Lock Time		5.0		ms
Charge Pump Output Current		5.0		mA
Output Power -	0.0		4.0	dBm
Phase Noise:				
1 kHz		-81	-78	dBc/Hz
10 kHz		-106	-103	dBc/Hz
100 kHz		-128	-125	dBc/Hz
Spurious Product - 200KHz		-80	-70	dBc
Reference Feedthrough -		-80	-70	dBc
Harmonic Suppression -2nd		-15		dBc
Output Impedance -		50		Ω
OperatingTemperature Range	-40		+85	℃
Package Type	PLL-15(15.24X15.24X3.5mm)			
POWER SUPPLY REQUIREMENTS				
Supply Voltage	3.15	3.3	3.45	Vdc
Supply Current		21	24	mA
NOTES				
Reference Input Frequency	5	10	104	MHz
Reference Input Voltage	0.4		3.0	Vp-p
Frequency Synthesizer IC	ADF4113			



Date: 30.OCT.2009 13:32:31

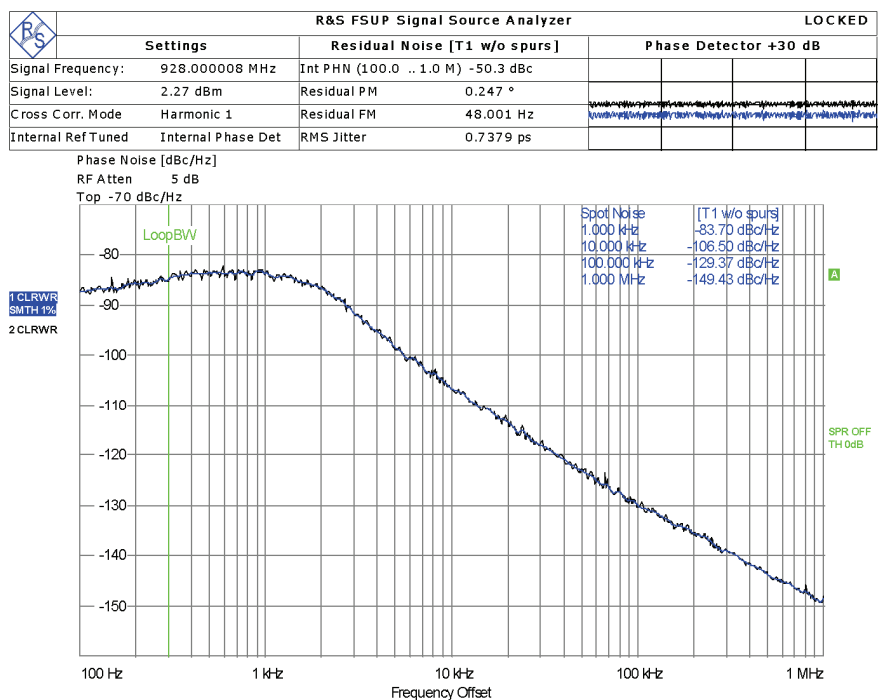


Date: 30.OCT.2009 13:35:13



Measurement Aborted

Date: 30.OCT.2009 13:29:56



Running ...

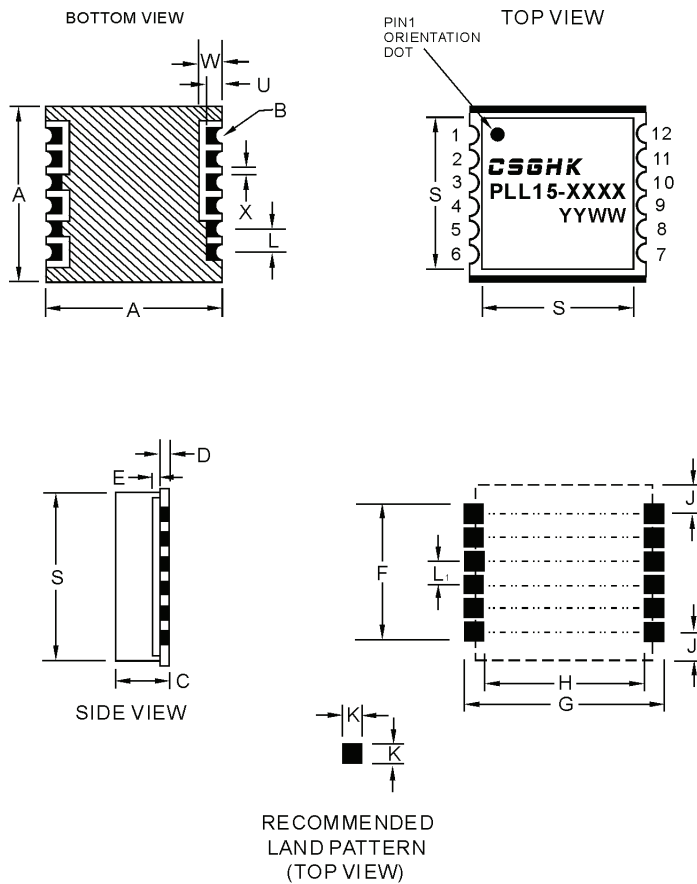
Date: 30.OCT.2009 13:30:39

REVISIONS

NOTE. UNLESS OTHERWISE SPECIFIED:

1. THE METAL CASE IS GROUND
2. ALL HALF VIA CONTACTS ARE PLATED THRU FOROM THE PAD ON THE TOP SIDE TO THE PAD ON THE BOTTOM SIDE OF THE BOARD.
3. HATCHED AREAS GROUND AND ARE COVERED WINTH LPI SOLDER MASK OVER BARE COPPER. ALL CONTACT AREAS ARE PLATED. SIGNAL VIAS MAY BE LOCATED WITHIN GROUND PLANE.
4. SUBSTRATE MATERIAL: FR-4
5. XXXX PEPRFSENTS THE MODEL NUMBER.
6. YYWW IS THE DATA CODE.

PIN OUT FOR PLL	
PIN	APPLICATION
1	CLOCK
2	DATA
3	ENABLE
4	DSC IN
6	GROUND
7	VCC (VCD)
9	RF OUT
11	LOCK DETECT
12	VCC (CHIP)



ALL OTHER PINS ARE GROUND

SYMBOL	DIMENSION	
	MILLIMETERS	INCHES
A	15.24±0.25	0.60±0.010
B	R=0.50	R=0.020
C	3.50	0.138
D	0.99MAX	0.039MAX
E	0.51	0.020
F	11.68	0.460
G	17.07	0.672
H	14.02	0.552
J	2.54	0.100
K	1.50	0.059
L	2.00±0.05	0.080±0.002
L ₁	2.00	0.080
S	14.60	0.575
U	1.28	0.050
W	1.78	0.070
X	0.51	0.020

	APPROVALS	DATA	 PLL-15 PACKAGE MECHANICAL PCB OUTLINE
DRAWN BY			
CHECKED BY			
PROCESS ENG			
QUALITY			
APPROVAL			