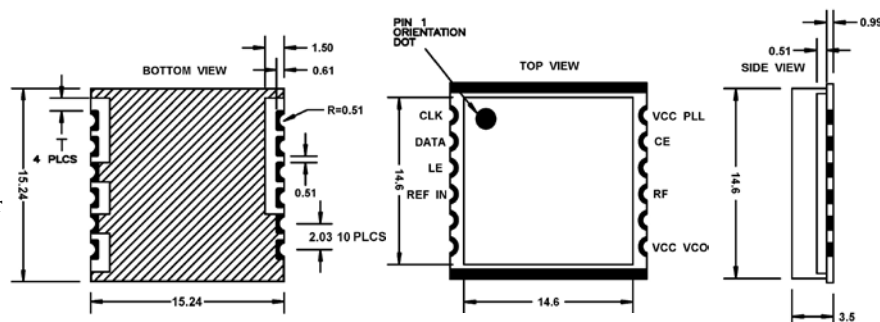


Features

- ◆ Low Cost
- ◆ Low Power Consumption
- ◆ Low Phase Noise
- ◆ Fast Settling Time
- ◆ Integrated VCO, PLL IC, Loop Filter

Applications

- ◆ Commercial Wireless Applications



Electrical Specifications, $T_A = +25^\circ\text{C}$, $V_{CC} = +5\text{V}$ (unless otherwise stated)

Parameter	Units	Min	Typ	Max
Frequency Range –	MHz	902		928
Step Size	kHz		200	
Settling Time – To within 1 kHz	mSec		5	10
Output Power –	dBm	1.0		3.0
Output Phase Noise:				
1 kHz	dBc/Hz		-86	-80
10 kHz	dBc/Hz		-111	-106
100 kHz	dBc/Hz		-132	-128
Power Supply –	Volts	4.75	5	5.25
Supply Current	mA		22	28
Spurious Product – 200 kHz	dBc		-80	-75
Reference Feedthrough –	dBc		-80	-75
Harmonic Suppression:				
2nd Harmonic	dBc		-13	-10
3rd Harmonic	dBc		-20	-10
Ref Osc Signal				
Frequency	MHz		10	
Amplitude	Vp-p	3.0		5.0
Phase Noise – 1 kHz	dBc/Hz		-145	
Input Impedance	k Ω		100	
Output Impedance –	Ω		50	
Operating Temperature Range	$^\circ\text{C}$	-40		+85

Comments

All specifications apply with a 50 ohm load impedance.