



SURFACE MOUNT MODEL VOLTAGE CONTROLLED OSCILLATOR

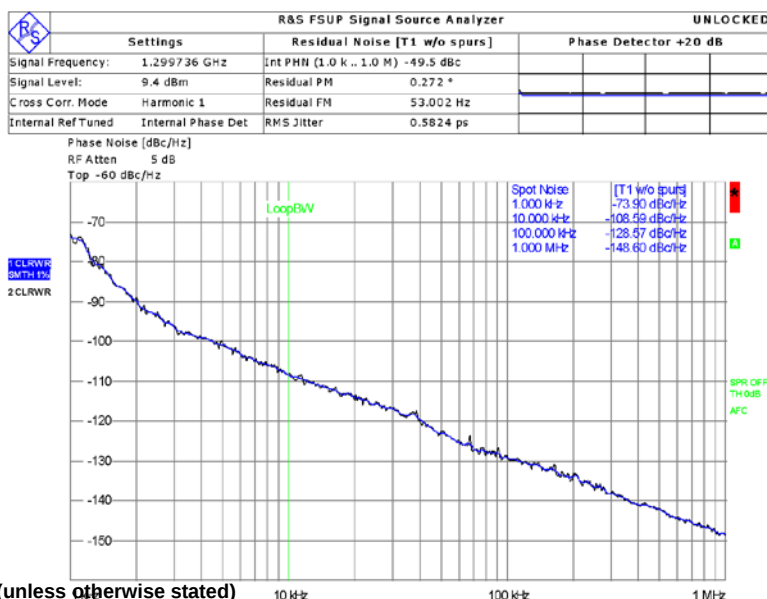
Frequency 1155-1442MHz

VL1442B



FEATURES

- ▶ Miniature Size
- ▶ Surface Mount Package
- ▶ Low Phase Noise Performance
- ▶ Electrically Shielded
- ▶ Highly Linear Tuning



Electrical Specifications, $T_A = +25^\circ\text{C}$, $V_{CC} = +12.0\text{V}$ (unless otherwise stated)

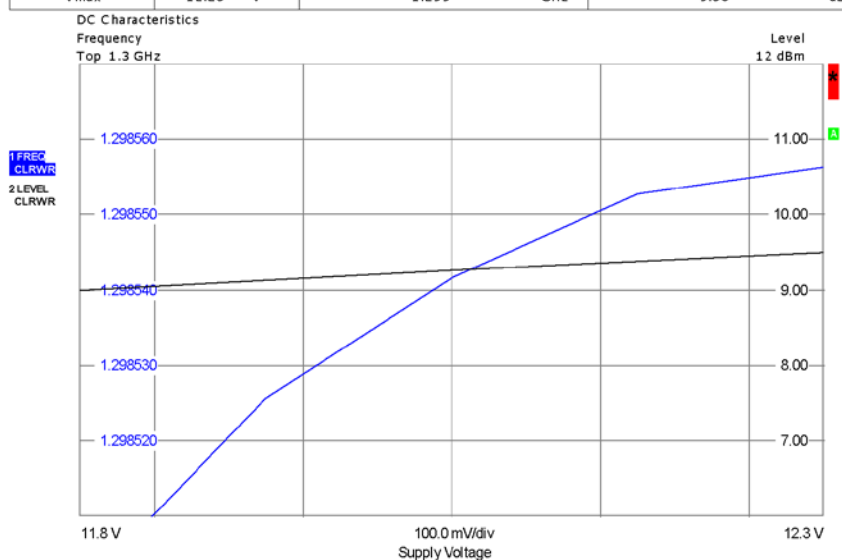
Parameter	Test Conditions	Min	Typ	Max	Units
Frequency Rangs		1155		1442	MHz
Tuning Voltage (V)		1.0		12.0	V
RF Output Power	1155-1442MHz	7.0		11.0	dBm
Supply Voltage (Vcc)		11.75	12	12.25	V
Supply Current (Icc)			26	29	mA
Phase Noise :					
	@10kHz Offset:		-107	-104	dBc/Hz
	@100kHz Offset:		-127	-124	dBc/Hz
Average Tuning Sensitivity	1155-1442MHz		35.3		MHz/V
Harmonic Outputs			-10		dBc
OperatingTemperature Range		-55		+85	°C
Tune Input Capacitance			27		PF
Output Impedance -			50		Ω
Package Type	SK605(12.7X12.7X4.3mm)				

Comments

X Indicates parameter to be tested 100% in production

V TUNE	TUNING SENS. (MHz/V)	FREQUENCY(MHz)			POWER OUTPUT(dBm)			HARMONIC(dBc)		
		-55℃	+25℃	+85℃	-55℃	+25℃	+85℃	-55℃	+25℃	+85℃
1.0	25.4	1124.5	1113.2	1109.0	11.5	9.4	7.9	-	-	-
2.0	24.8	1148.6	1138.9	1134.7	11.3	9.3	7.9	-	-	-
3.0	27.3	1175.5	1164.8	1160.3	11.1	9.3	8.0	-	-	-
4.0	32.4	1205.3	1193.9	1189.3	11.3	9.4	8.1	-	-	-
5.0	36.1	1237.3	1227.5	1223.0	11.4	9.3	8.0	-	-	-
6.0	38.8	1275.8	1265.0	1260.6	11.0	9.2	8.1	-	-	-
7.0	41.0	1313.9	1306.1	1301.2	11.3	9.0	8.0	-	-	-
8.0	41.2	1354.4	1346.2	1341.6	10.8	9.0	8.1	-	-	-
9.0	39.5	1393.4	1387.6	1382.9	11.1	8.7	7.9	-	-	-
10.0	38.3	1433.5	1425.9	1420.9	10.4	8.8	8.2	-	-	-
11.0	37.4	1473.0	1464.9	1458.7	10.1	8.6	8.1	-	-	-
12.0	36.2	1510.5	1501.8	1496.4	9.8	8.8	8.3	-	-	-

R&S	R&S FSUP Signal Source Analyzer					
	Settings		Results			
	Supply	Voltage	Frequency		Level	
	Vmin	11.75 V	1.298	GHz	8.98	dBm
	Vcurrent	12.00 V	1.299	GHz	9.26	dBm
	Vmax	12.25 V	1.299	GHz	9.50	dBm



Measurement Aborted

Date: 17.AUG.2009 18:00:15

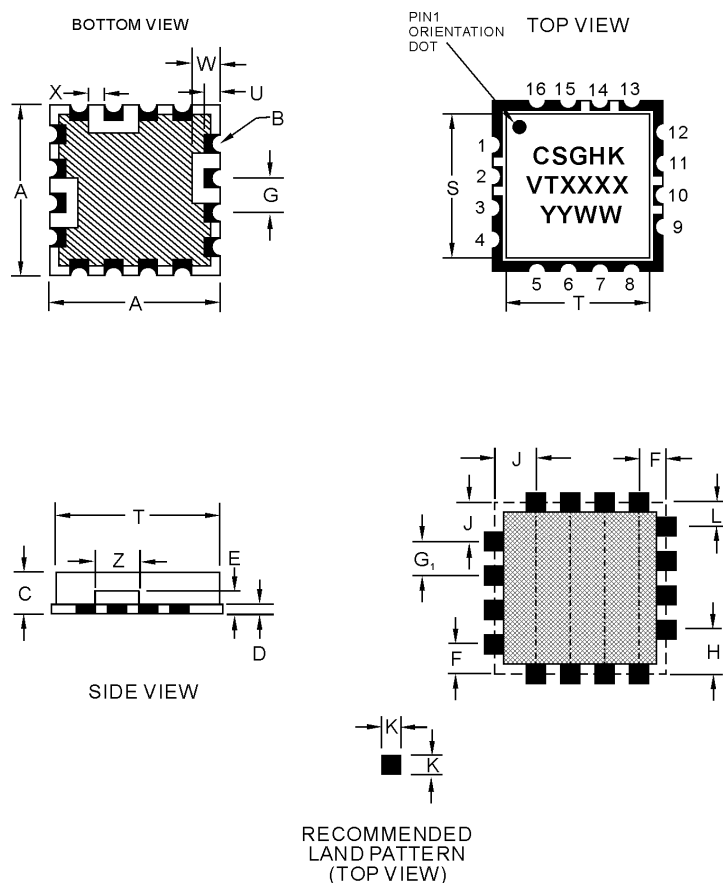
REVISIONS

NOTE. UNLESS OTHERWISE SPECIFIED:

1. THE METAL CASE IS GROUND
2. ALL HALF VIA CONTACTS ARE PLATED THRU FORM THE PAD ON THE TOP SIDE TO THE PAD ON THE BOTTOM SIDE OF THE BOARD.
3. HATCHED AREAS GROUND AND ARE COVERED WITH LPI SOLDER MASK OVER BARE COPPER. ALL CONTACT AREAS ARE PLATED. SIGNAL VIAS MAY BE LOCATED WITHIN GROUND PLANE.
4. SUBSTRATE MATERIAL: FR-4
5. XXXX REPRESENTS THE MODEL NUMBER.
6. YYWW IS THE DATA CODE.

PIN OUT FOR PLL	
PIN	APPLICATION
2	VT
10	RF OUT
14	VCC

ALL OTHER PINS ARE GROUND



SYMBOL	DIMENSION	
	MILLIMETERS	INCHES
A	12.7±0.25	0.50±0.010
B	R=0.50	R=0.019
C	3.50	0.138
D	0.99MAX	0.039MAX
E	1.75MAX	0.069MAX
F	2.16	0.085
G	2.54±0.08	0.100±0.003
G ₁	2.54	0.100
H	3.07	0.121
J	2.92	0.115
K	1.52	0.060
L	2.00	0.079
S	11.68	0.460
T	11.81	0.465
U	1.10	0.043
W	1.78	0.188
X	1.02	0.040
Z	3.49	0.137

	APPROVALS	DATA	 SK605 PACKAGE MECHANICAL PCB OUTLINE
DRAWN BY			
CHECKED BY			
PROCESS ENG			
QUALITY			