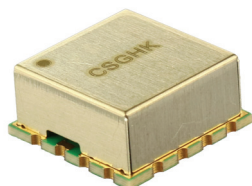


SURFACE MOUNT MODEL VOLTAGE CONTROLLED OSCILLATOR

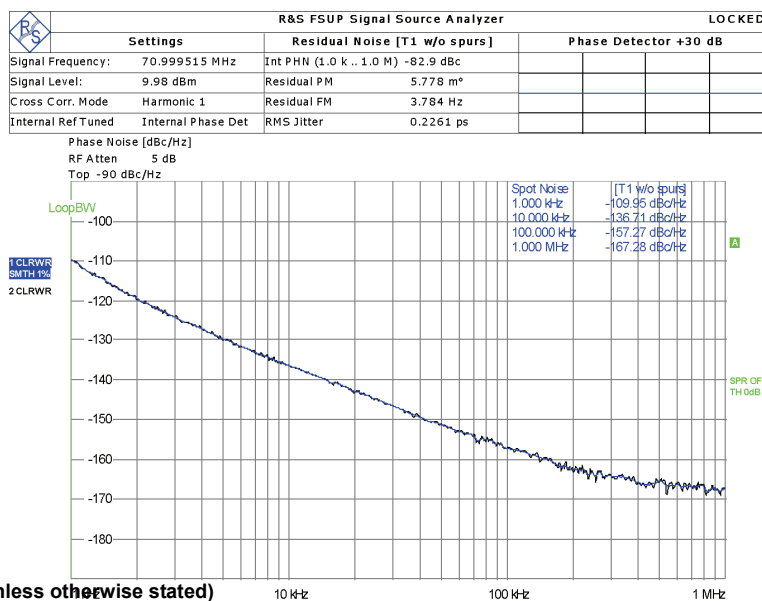
Frequency

VLN71M



FEATURES

- ▶ Miniature Size
- ▶ Surface Mount Package
- ▶ Low Phase Noise Performance
- ▶ Electrically Shielded
- ▶ Highly Linear Tuning



Electrical Specifications, $T_A = +25^\circ\text{C}$, $V_{CC} = +5.0\text{V}$ (unless otherwise stated)

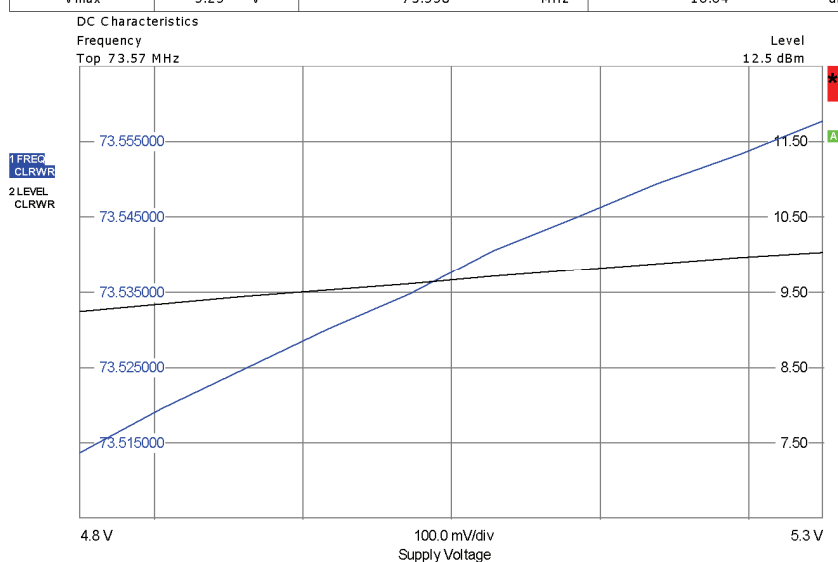
Parameter	Test Conditions	Min	Typ	Max	Units
Frequency Rangs			71		MHz
Tuning Voltage (V)		0.5		4.5	V
RF Output Power		7.0		11.0	dBm
Supply Voltage (Vcc)		4.75	5	5.25	V
Supply Current (Icc)			23	26	mA
Phase Noise :					
	@10kHz Offset:		-135	-132	dBc/Hz
	@100kHz Offset:		-155	-152	dBc/Hz
Average Tuning Sensitivity			0.9		MHz/V
Harmonic Outputs			-20		dBc
OperatingTemperature Range		-55		+85	°C
Tune Input Capacitance			1000		PF
Output Impedance -			50		Ω
Package Type	SK605G(12.7X12.7X5.8mm)				

Comments

X Indicates parameter to be tested 100% in production

V TUNE	TUNING SENS. (MHz/V)	FREQUENCY(MHz)			POWER OUTPUT(dBm)			HARMONIC(dBc)		
		-55℃	+25℃	+85℃	-55℃	+25℃	+85℃	-55℃	+25℃	+85℃
0.5	1.1	69.7	69.1	68.7	9.4	9.1	8.5	-18.1	-22.6	-28.7
1.0	1.0	70.1	69.6	69.2	9.5	9.2	8.5	-18.6	-23.0	-29.0
1.5	0.9	70.6	70.0	69.6	9.5	9.2	8.6	-18.8	-23.3	-29.3
2.0	0.9	71.1	70.5	70.1	9.5	9.2	8.6	-19.1	-23.4	-29.5
2.5	0.9	71.5	70.9	70.5	9.5	9.2	8.5	-19.3	-23.6	-29.7
3.0	0.9	72.0	71.3	70.9	9.5	9.2	8.6	-19.5	-23.9	-30.1
3.5	0.9	72.4	71.8	71.3	9.6	9.2	8.6	-19.9	-24.1	-30.5
4.0	0.9	72.9	72.2	71.8	9.6	9.2	8.6	-20.1	-24.4	-30.7
4.5	1.0	73.4	72.7	72.2	9.6	9.2	8.6	-20.4	-24.7	-31.0

R&S FSUP Signal Source Analyzer					
Settings			Results		
Supply	Voltage		Frequency		Level
Vmin	4.75 V		73.514 MHz		9.24 dBm
Vcurrent	5.00 V		73.537 MHz		9.66 dBm
Vmax	5.25 V		73.558 MHz		10.04 dBm



Measurement Aborted

Date: 5.SEP.2009 14:55:46

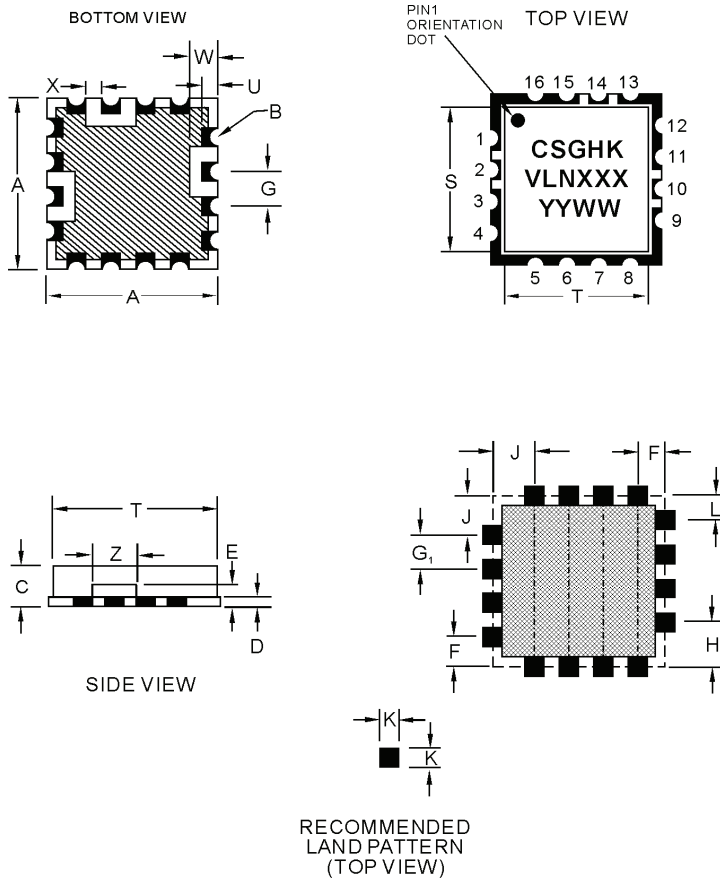
REVISIONS

NOTE. UNLESS OTHERWISE SPECIFIED:

1. THE METAL CASE IS GROUND
2. ALL HALF VIA CONTACTS ARE PLATED THRU FORM THE PAD ON THE TOP SIDE TO THE PAD ON THE BOTTOM SIDE OF THE BOARD.
3. HATCHED AREAS GROUND AND ARE COVERED WITH LPI SOLDER MASK OVER BARE COPPER. ALL CONTACT AREAS ARE PLATED. SIGNAL VIAS MAY BE LOCATED WITHIN GROUND PLANE.
4. SUBSTRATE MATERIAL: FR-4
5. XXXX REPRESENTS THE MODEL NUMBER.
6. YYWW IS THE DATA CODE.

PIN OUT FOR PLL	
PIN	APPLICATION
2	VT
10	RF OUT
14	VCC

ALL OTHER PINS ARE GROUND



SYMBOL	DIMENSION	
	MILLIMETERS	INCHES
A	12.7±0.25	0.50±0.010
B	R=0.50	R=0.019
C	5.80	0.228
D	0.99MAX	0.039MAX
E	1.75MAX	0.069MAX
F	2.16	0.085
G	2.54±0.08	0.100±0.003
G ₁	2.54	0.100
H	3.07	0.121
J	2.92	0.115
K	1.52	0.060
L	2.00	0.079
S	11.68	0.460
T	11.81	0.465
U	1.10	0.043
W	1.78	0.188
X	1.02	0.040
Z	3.49	0.137

	APPROVALS	DATA	 SK605G PACKAGE MECHANICAL PCB OUTLINE
DRAWN BY			
CHECKED BY			
PROCESS ENG			
QUALITY			