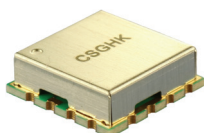


SURFACE MOUNT MODEL VOLTAGE CONTROLLED OSCILLATOR

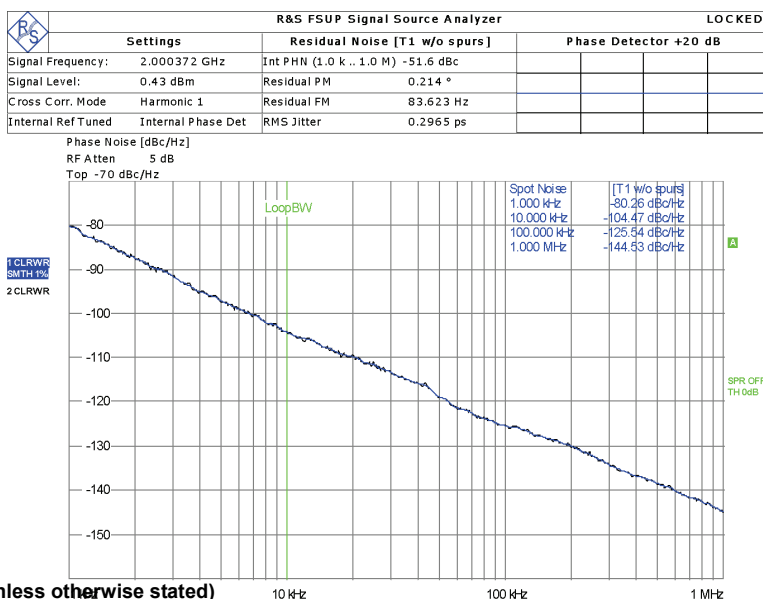
Frequency 1970-2030MHz

VT2030M



FEATURES

- ▶ Miniature Size
- ▶ Surface Mount Package
- ▶ Low Phase Noise Performance
- ▶ Electrically Shielded
- ▶ Highly Linear Tuning



Electrical Specifications, $T_A = +25^\circ\text{C}$, $V_{CC} = +5.0\text{V}$ (unless otherwise stated)

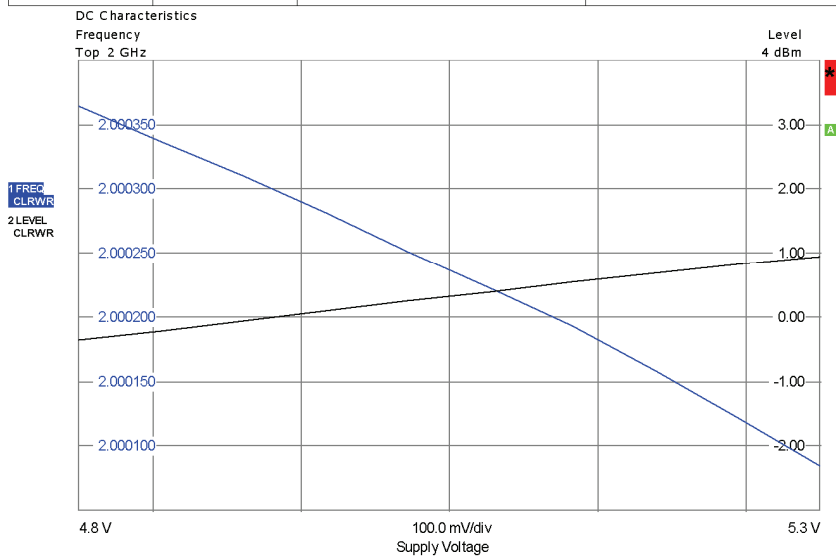
Parameter	Test Conditions	Min	Typ	Max	Units
Frequency Rangs		1970		2030	MHz
Tuning Voltage (V)		0.5		4.5	V
RF Output Power	1970-2030MHz	-2.0		2.0	dBm
Supply Voltage (Vcc)		4.75		5.25	V
Supply Current (Icc)			12	15	mA
Phase Noise :					
	@10kHz Offset:		-104	-101	dBc/Hz
	@100kHz Offset:		-125	-122	dBc/Hz
Average Tuning Sensitivity	1970-2030MHz		25.9		MHz/V
Harmonic Outputs			-15		dBc
OperatingTemperature Range		-40		+85	°C
Tune Input Capacitance			18		PF
Output Impedance -			50		Ω
Package Type	SK605(12.7X12.7X4.3mm)				

Comments

X Indicates parameter to be tested 100% in production

V TUNE	TUNING SENS. (MHz/V)	FREQUENCY(MHz)			POWER OUTPUT(dBm)			HARMONIC(dBc)		
		-40℃	+25℃	+85℃	-40℃	+25℃	+85℃	-40℃	+25℃	+85℃
0.5	28.9	1962.8	1949.6	1939.0	0.1	0.3	-0.6	-15.4	-19.2	-23.8
1.0	27.2	1977.2	1964.0	1953.2	0.1	0.2	-0.5	-15.1	-19.5	-24.1
1.5	26.0	1990.6	1977.2	1966.4	0.1	0.3	-0.5	-15.3	-19.2	-24.3
2.0	25.7	2003.6	1990.2	1979.4	0.1	0.2	-0.6	-15.8	-19.1	-23.9
2.5	25.6	2016.6	2003.2	1992.0	0.0	0.2	-0.6	-16.2	-19.7	-23.7
3.0	25.6	2029.6	2016.0	2004.8	0.0	0.1	-0.6	-15.8	-20.0	-24.2
3.5	25.5	2042.6	2028.8	2017.4	-0.1	0.1	-0.6	-15.9	-19.7	-24.7
4.0	24.5	2055.0	2041.2	2030.2	-0.2	0.1	-0.7	-16.6	-19.8	-24.2
4.5	23.0	2067.0	2053.2	2042.0	-0.4	0.1	-0.7	-17.0	-20.3	-24.2

BS	R&S FSUP Signal Source Analyzer					
	Settings		Results			
	Supply	Voltage	Frequency		Level	
	Vmin	4.75 V	2.000	GHz	-0.37	dBm
	Vcurrent	5.00 V	2.000	GHz	0.32	dBm
	Vmax	5.25 V	2.000	GHz	0.95	dBm



Measurement Aborted

Date: 3.DEC.2010 14:05:44

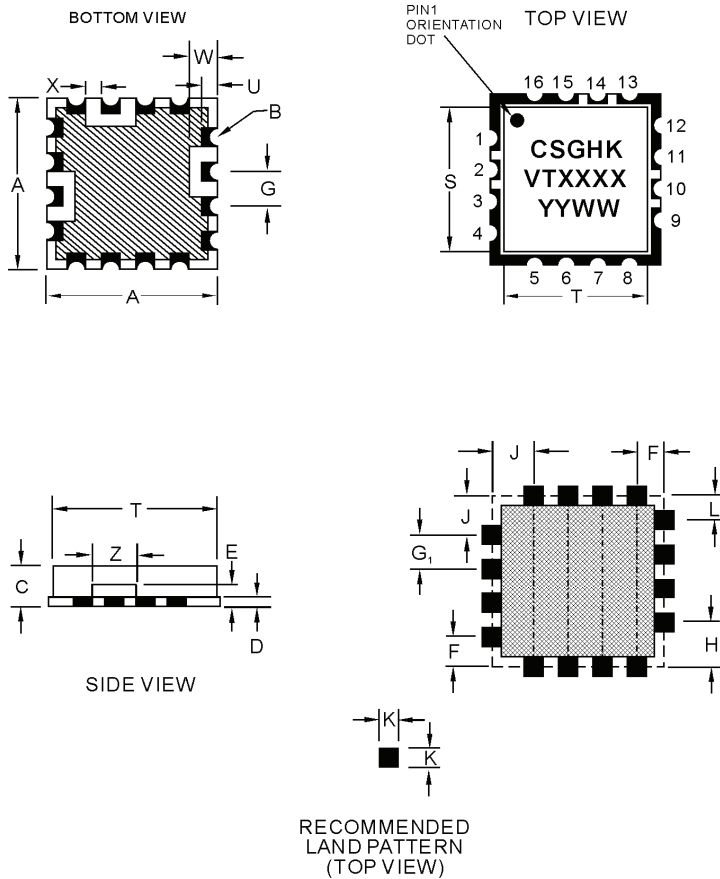
REVISIONS

NOTE. UNLESS OTHERWISE SPECIFIED:

1. THE METAL CASE IS GROUND
2. ALL HALF VIA CONTACTS ARE PLATED THRU FROM THE PAD ON THE TOP SIDE TO THE PAD ON THE BOTTOM SIDE OF THE BOARD.
3. HATCHED AREAS GROUND AND ARE COVERED WITH LPI SOLDER MASK OVER BARE COPPER. ALL CONTACT AREAS ARE PLATED. SIGNAL VIAS MAY BE LOCATED WITHIN GROUND PLANE.
4. SUBSTRATE MATERIAL: FR-4
5. XXXX REPRESENTS THE MODEL NUMBER.
6. YYWW IS THE DATA CODE.

PIN OUT FOR PLL	
PIN	APPLICATION
2	VT
10	RF OUT
14	VCC

ALL OTHER PINS ARE GROUND



SYMBOL	DIMENSION	
	MILLIMETERS	INCHES
A	12.7±0.25	0.50±0.010
B	R=0.50	R=0.019
C	4.3	0.169
D	0.99MAX	0.039MAX
E	1.75MAX	0.069MAX
F	2.16	0.085
G	2.54±0.08	0.100±0.003
G ₁	2.54	0.100
H	3.07	0.121
J	2.92	0.115
K	1.52	0.060
L	2.00	0.079
S	11.68	0.460
T	11.81	0.465
U	1.10	0.043
W	1.78	0.188
X	1.02	0.040
Z	3.49	0.137

	APPROVALS	DATA	 SK605 PACKAGE MECHANICAL PCB OUTLINE
DRAWN BY			
CHECKED BY			
PROCESS ENG			
QUALITY			