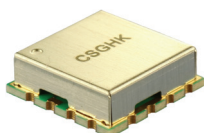


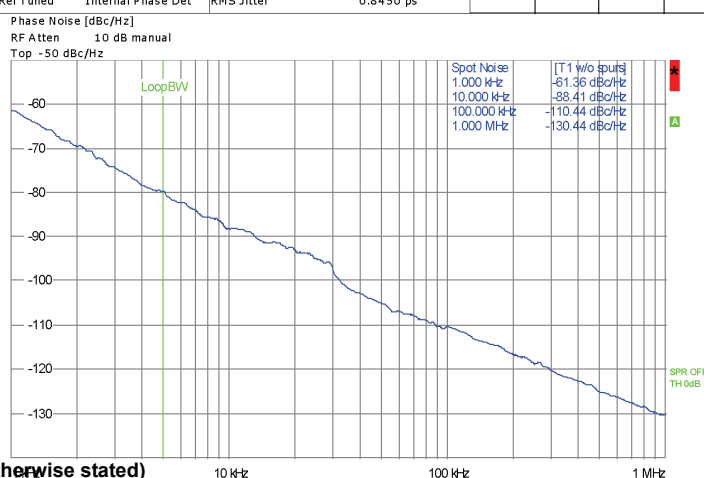
SURFACE MOUNT MODEL VOLTAGE CONTROLLED OSCILLATOR

Frequency 5595-5700MHz

VT5700M



R&S FSUP Signal Source Analyzer				LOCKED			
Settings		Residual Noise [T1 w/o spurs]		Phase Detector +0 dB			
Signal Frequency:	5.652146 GHz	Int PHN (1.0 k .. 1.0 M)	-33.5 dBc				
Signal Level:	-0.56 dBm	Residual PM	1.719 °				
Cross Corr Mode	Harmonic 1	Residual FM	421.469 Hz				
Internal Ref Tuned	Internal Phase Det	RMS Jitter	0.8450 ps				



FEATURES

- ▶ Miniature Size
- ▶ Surface Mount Package
- ▶ Low Phase Noise Performance
- ▶ Electrically Shielded
- ▶ Highly Linear Tuning

Electrical Specifications, $T_A = +25^\circ\text{C}$, $V_{CC} = +5\text{V}$ (unless otherwise stated)

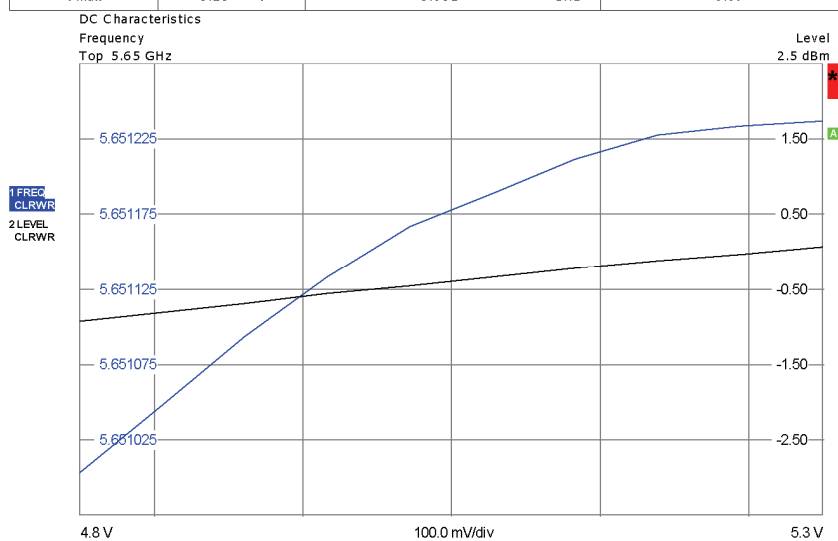
Parameter	Test Conditions	Min	Typ	Max	Units
Frequency Rangs		5595		5700	MHz
Tuning Voltage (V)		0.5		4.5	V
RF Output Power	5595-5700MHz	-2.0		2.0	dBm
Supply Voltage (Vcc)		4.75	5.0	5.25	V
Supply Current (Icc)			36	40	mA
Phase Noise :					
	@10kHz Offset:		-87	-84	dBc/Hz
	@100kHz Offset:		-110	-107	dBc/Hz
Average Tuning Sensitivity	5595-5700MHz		101		MHz/V
Harmonic Outputs			-20		dBc
Operating Temperature Range		-40		+85	°C
Tune Input Capacitance			8.3		PF
Output Impedance -			50		Ω
Package Type	SK605(12.7X12.7X3.5mm)				

Comments

X Indicates parameter to be tested 100% in production

V TUNE	TUNING SENS. (MHz/V)	FREQUENCY(MHz)			POWER OUTPUT(dBm)			HARMONIC(dBc)		
		-40℃	+25℃	+85℃	-40℃	+25℃	+85℃	-40℃	+25℃	+85℃
0.5	90.5	5411.0	5392.0	5379.0	-1.6	-1.0	-1.6	22.7	-20.9	-24.5
1.0	88.7	5452.4	5435.2	5425.4	-1.0	-1.3	-2.3	-28.9	-21.7	-23.2
1.5	85.2	5499.0	5480.0	5465.2	-1.5	-0.9	-1.7	-22.4	-20.4	-26.4
2.0	88.2	5538.4	5524.8	5513.0	-1.5	-1.6	-1.3	-28.9	-19.2	-22.3
2.5	100.8	5588.4	5570.2	5554.2	-1.0	-0.9	-2.1	-24.8	-20.2	-25.2
3.0	104.8	5632.6	5620.0	5606.0	-1.6	-1.4	-1.6	30.9	-19.8	-21.2
3.5	97.9	5688.4	5670.6	5652.4	-1.1	-1.1	-2.2	-27.7	-22.2	-26.5
4.0	104.8	5734.8	5722.6	5707.6	-1.5	-1.4	-1.5	-38.3	-22.1	-22.6
4.5	104.8	5790.2	5774.4	5755.4	-0.8	-1.0	-2.1	-30.1	-21.9	-27.0

B6	R&S FSUP Signal Source Analyzer					
	Settings			Results		
	Supply	Voltage		Frequency		Level
	Vmin	4.75	V	5.651	GHz	-0.94 dBm
	Vcurrent	5.00	V	5.651	GHz	-0.41 dBm
	Vmax	5.25	V	5.651	GHz	0.07 dBm



Measurement Aborted

Date: 5.JUL.2012 09:46:01

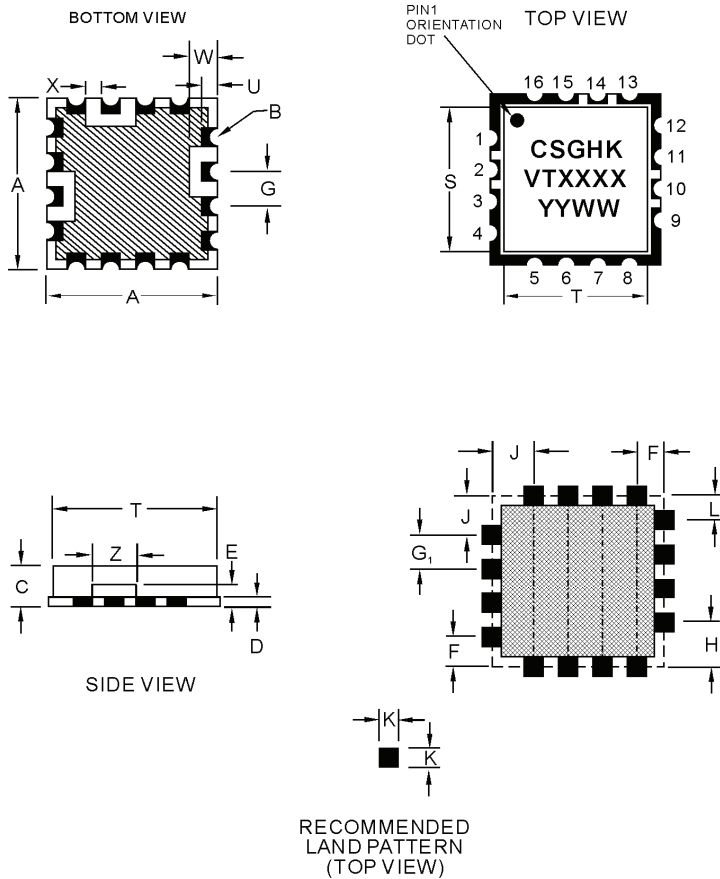
REVISIONS

NOTE. UNLESS OTHERWISE SPECIFIED:

1. THE METAL CASE IS GROUND
2. ALL HALF VIA CONTACTS ARE PLATED THRU FROM THE PAD ON THE TOP SIDE TO THE PAD ON THE BOTTOM SIDE OF THE BOARD.
3. HATCHED AREAS GROUND AND ARE COVERED WITH LPI SOLDER MASK OVER BARE COPPER. ALL CONTACT AREAS ARE PLATED. SIGNAL VIAS MAY BE LOCATED WITHIN GROUND PLANE.
4. SUBSTRATE MATERIAL: FR-4
5. XXXX REPRESENTS THE MODEL NUMBER.
6. YYWW IS THE DATA CODE.

PIN OUT FOR PLL	
PIN	APPLICATION
2	VT
10	RF OUT
14	VCC

ALL OTHER PINS ARE GROUND



SYMBOL	DIMENSION	
	MILLIMETERS	INCHES
A	12.7±0.25	0.50±0.010
B	R=0.50	R=0.019
C	3.5	0.138
D	0.99MAX	0.039MAX
E	1.75MAX	0.069MAX
F	2.16	0.085
G	2.54±0.08	0.100±0.003
G ₁	2.54	0.100
H	3.07	0.121
J	2.92	0.115
K	1.52	0.060
L	2.00	0.079
S	11.68	0.460
T	11.81	0.465
U	1.10	0.043
W	1.78	0.188
X	1.02	0.040
Z	3.49	0.137

	APPROVALS	DATA	 SK605 PACKAGE MECHANICAL PCB OUTLINE
DRAWN BY			
CHECKED BY			
PROCESS ENG			
QUALITY			