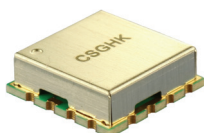


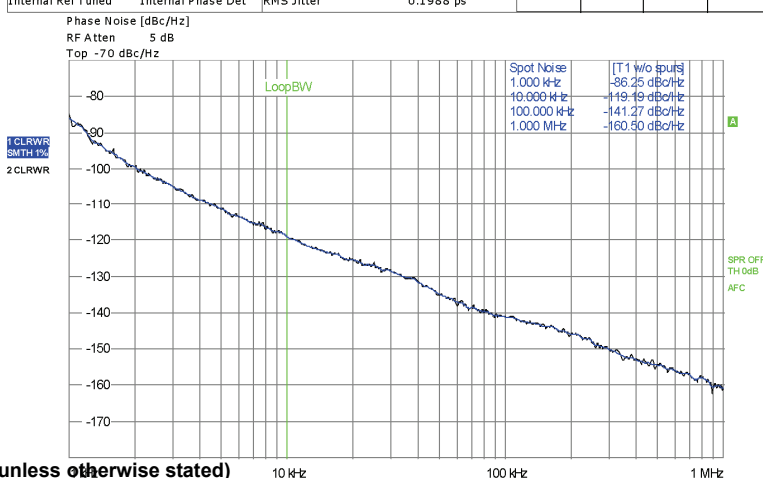
SURFACE MOUNT MODEL VOLTAGE CONTROLLED OSCILLATOR

Frequency

VT951M



R&S FSUP Signal Source Analyzer				LOCKED
Settings		Residual Noise [T1 w/o spurs]		Phase Detector +20 dB
Signal Frequency:	951.397788 MHz	Int PHN (1.0 k .. 1.0 M)	-61.5 dBc	
Signal Level:	8.37 dBm	Residual PM	68.073 m°	
Cross Corr. Mode	Harmonic 1	Residual FM	13.374 Hz	
Internal Ref Tuned	Internal Phase Det	RMS Jitter	0.1988 ps	



FEATURES

- ▶ Miniature Size
- ▶ Surface Mount Package
- ▶ Low Phase Noise Performance
- ▶ Electrically Shielded
- ▶ Highly Linear Tuning

Electrical Specifications, $T_A = +25^\circ\text{C}$, $V_{CC} = +12.0\text{V}$ (unless otherwise stated)

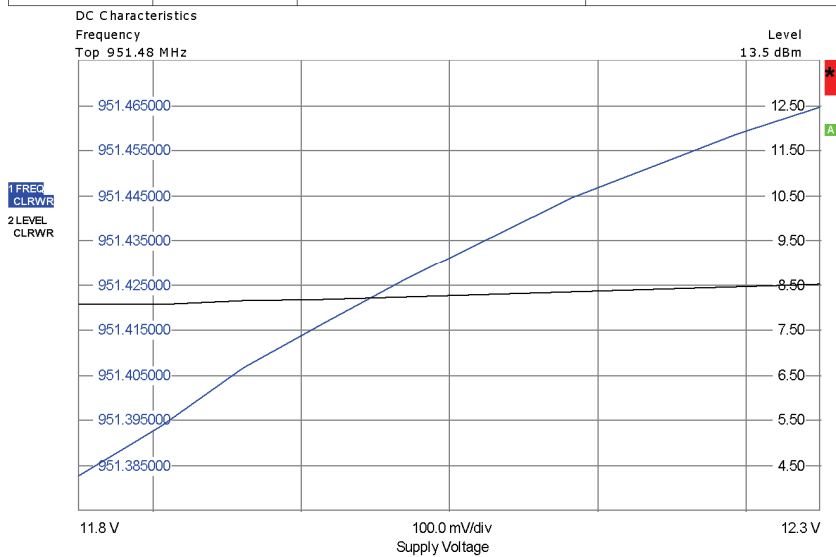
Parameter	Test Conditions	Min	Typ	Max	Units
Frequency Rangs			951.4		MHz
Tuning Voltage (V)		0.5		4.5	V
RF Output Power		7.0		11.0	dBm
Supply Voltage (Vcc)		11.75		12.25	V
Supply Current (Icc)			27	30	mA
Phase Noise :					
	@10kHz Offset:		-118	-115	dBc/Hz
	@100kHz Offset:		-139	-136	dBc/Hz
Average Tuning Sensitivity			7.2		MHz/V
Harmonic Outputs			-10		dBc
Operating Temperature Range		-55		+85	$^\circ\text{C}$
Tune Input Capacitance			39		PF
Output Impedance -			50		Ω
Package Type	SK605(12.7X12.7X4.3mm)				

Comments

X Indicates parameter to be tested 100% in production

V TUNE	TUNING SENS. (MHz/V)	FREQUENCY(MHz)			POWER OUTPUT(dBm)			HARMONIC(dBc)		
		-55℃	+25℃	+85℃	-55℃	+25℃	+85℃	-55℃	+25℃	+85℃
0.5	11.3	940.6	935.1	930.3	9.4	9.1	8.5	-13.4	-13.2	-13.5
1.0	9.3	945.7	940.4	935.4	9.5	9.1	8.4	-13.6	-13.2	-13.3
1.5	8.1	949.9	944.7	939.7	9.4	9.1	8.4	-13.8	-13.2	-13.3
2.0	7.5	953.9	948.4	943.6	9.4	9.0	8.3	-13.8	-13.3	-13.2
2.5	7.2	957.4	952.1	947.1	9.4	9.0	8.3	-13.9	-13.4	-13.3
3.0	7.0	961.0	955.6	950.6	9.4	9.0	8.3	-13.9	-13.6	-13.5
3.5	6.9	964.4	959.0	954.0	9.5	9.1	8.3	-13.9	-13.7	-13.6
4.0	6.8	967.9	962.4	957.3	9.5	9.1	8.2	-13.9	-13.7	-13.6
4.5	6.8	971.4	965.7	960.6	9.4	9.0	8.2	-13.7	-13.5	-13.6

BS	R&S FSUP Signal Source Analyzer					
	Settings		Results			
	Supply	Voltage	Frequency		Level	
	Vmin	11.75 V	951.383	MHz	8.05	dBm
	Vcurrent	12.00 V	951.432	MHz	8.26	dBm
	Vmax	12.25 V	951.465	MHz	8.50	dBm



Measurement Aborted

Date: 18.JAN.2010 16:27:08

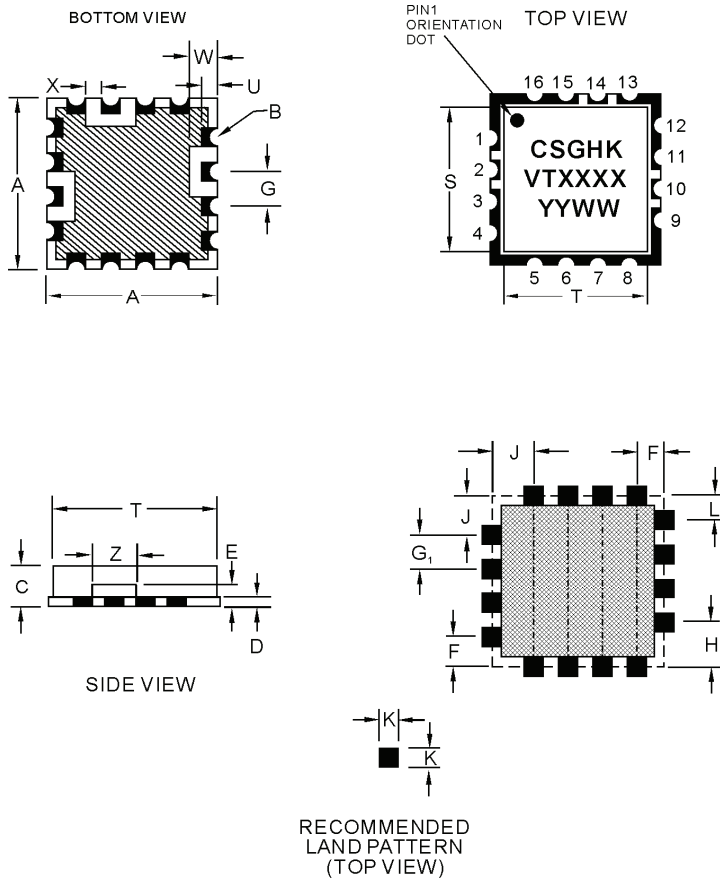
REVISIONS

NOTE, UNLESS OTHERWISE SPECIFIED:

1. THE METAL CASE IS GROUND
2. ALL HALF VIA CONTACTS ARE PLATED THRU FROM THE PAD ON THE TOP SIDE TO THE PAD ON THE BOTTOM SIDE OF THE BOARD.
3. HATCHED AREAS GROUND AND ARE COVERED WITH LPI SOLDER MASK OVER BARE COPPER. ALL CONTACT AREAS ARE PLATED. SIGNAL VIAS MAY BE LOCATED WITHIN GROUND PLANE.
4. SUBSTRATE MATERIAL: FR-4
5. XXXX REPRESENTS THE MODEL NUMBER.
6. YYWW IS THE DATA CODE.

PIN OUT FOR PLL	
PIN	APPLICATION
2	VT
10	RF OUT
14	VCC

ALL OTHER PINS ARE GROUND



SYMBOL	DIMENSION	
	MILLIMETERS	INCHES
A	12.7±0.25	0.50±0.010
B	R=0.50	R=0.019
C	4.3	0.169
D	0.99MAX	0.039MAX
E	1.75MAX	0.069MAX
F	2.16	0.085
G	2.54±0.08	0.100±0.003
G ₁	2.54	0.100
H	3.07	0.121
J	2.92	0.115
K	1.52	0.060
L	2.00	0.079
S	11.68	0.460
T	11.81	0.465
U	1.10	0.043
W	1.78	0.188
X	1.02	0.040
Z	3.49	0.137

	APPROVALS	DATA	 SK605 PACKAGE MECHANICAL PCB OUTLINE
DRAWN BY			
CHECKED BY			
PROCESS ENG			
QUALITY			